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Application Number		10644210	
Filing Date		2003-08-20	
irst Named Inventor	Jude /	A. Rivers	
Art Unit		2189	
xaminer Name	Reba	I. ELMORE	
Attorney Docket Number		YOR920030249US1	
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

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	1	J. A. Rivers et al. "Reducing conflicts in direct-mapped caches with a temporality-based design." In Proceedings of the 1996 ICPP, pages 154-163, August 1996.							
	2	J. A. Rivers et al. "On effective data supply for multi-issue processors." In Proceedings of ICCD'97, pages 519-528, October 1997.							
	3	J. A. Rivers et al. "Utilizing reuse information in data cache management." In Proceedings of ICIS'98, pages 449-456, 1998.							
	4	Edward S. Tam et al. "Evaluating the Performance of Active Cache Management Schemes" Proceedings of the 1998 International Conference on Computer Design, October 1998.							
	5	J.A. Rivers et al., "On High-Bandwidth Data Cache Design for Multi-Issue Processors," Proc. 30th Int'l Symp. Microarchitecture, pp. 46-56, Dec. 1997.							
	6	Edward S. Tam et al. "Active Management of Data Caches by Exploiting Reuse Information" IEEE Transactions on Computers, Vol. 48, No. 11, pp. 1244 - 1259, November 1999.							
	7 J. A. RIVERS, Performance Aspects of High-Bandwidth Multi-Lateral Cache Organizations, 4/1/1998								
	8	J.A. Rivers et al., "Performance Issues in Integrating Temporality-Based Caching" Elsevier, 8/26/1996.							
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